

Rate Contract
Government of India
Directorate General of Supplies & Disposals
Jeevan Tara Building, 5 Sansad Marg
New Delhi-110001
Tel Number 23360610/23360537

Digitally signed by:
Bahadur Singh Khati
Email Id:
bskhati@dgsnd.com.in

Rate Contract no. LOGIC_ANA/IT-4/RC-D2070000/0512/84/F0372/1541
Dated 04-MAY-11

To,
SYNERGY MEASUREMENT TECHNOLOGIES PVT. LTD.
25/C, Nicholson Road, Tarbund, Secunderabad-500009 (AP)
E-mail: debashis@synergytech.in
Tele No. 040-44444040/50/35 Mr. Debashis: 09830067183

Sub: Rate Contract for supply of Logic Analyzer
Validity: From 01-JUN-11 To 31-MAY-12 .

Ref:(1) This Office Tender Enquiry No. LOGIC_ANA/IT-4/RC-D2070000/0512/84
Opened on 15-FEB-11.
(2) Your Quotation No. And Dated

Dear Sir,

You are hereby informed that your above referred tender read with subsequent letters mentioned above for the Stores specified in the Schedules annexed has been accepted. This rate contract will be governed by the terms and conditions brought in the Form no. DGS&D 1001 available from dgs&d sales counter on payment of Rs. 50/- .The Rate Contract and the schedules annexed here to shall be the sole repository of this Rate Contract/Transaction.

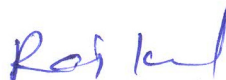
SCHEDULES ANNEXED

- 1.Schedule "A" Description of stores,prices,duties/taxes.
- 2.Schedule "B" special conditions of contract / Other information.
- 3.Schedule "C" Information to DDOs about parallel rate contracts.
- 4.Annexure - Technical Specification

Yours Faithfully,



Assistant Director(S)/Section Officer/Dy.Director
For and on behalf of the purchaser named in the Form DGS&D 1001.



COPY TO:

- 1.The Chief Controller of Accounts,Department of commerce,New Delhi/
COA Mumbai/COA Kolkata,COA Chennai.
(Through authentication cell) This issues with the approval of competent authority.
- 2.Deputy Director General of Supplies & Disposals,Chennai -10 copies each
- 3.Deputy Director General of Supplies & Disposals,Kolkata -10 copies each
- 4.Deputy Director General of Supplies & Disposals,Mumbai -10 copies each
- 5.Deputy Director General (QA),Kolkata -10 copies each
- 6.Deputy Director General (QA),Chennai -10 copies each
- 7.Deputy Director General (QA),Mumbai -10 copies each
- 8.Deputy Director General (QA),DGS&D -10 copies each
- 9.Inspection Authority ADG(QA) DGS&D New Delhi - 110001.
- 10.Quality assurance Officer Director (QA) Delhi Directorate/ Mumbai,Chennai/Kolkata.
- 11.Concurrent Audit
- 12.MIS Cell
- 13.Ledger clerk
- 14.O.L.Section
- 15.All Direct Demanding Officers as per mailing list maintained by DGS&D.

(Raj K)

Assistant Director (S) / Section Officer / Deputy Director
FOR DIRECTOR GENERAL OF SUPPLIES & DISPOSALS

Raj K

SCHEDULE - A

1. Rate Contract No.: -LOGIC_ANA/IT-4/RC-D2070000/0512/84/F0372/1541

Dated 04-MAY-11 For the Supply of Logic Analyzer

2. Advance Rate Contract No.: - Nil

Dated

3.(a) Name and Full Address of the Firm :-

SYNERGY MEASUREMENT TECHNOLOGIES PVT.
LTD.

25/C, Nicholson Road, Tarbund, Secunderabad-500009 (AP)

E-mail: debashis@synergytech.in

Tele No. 040-44444040/50/35 Mr. Debashis: 09830067183

ANDHRA PRADESH - 500015

Tel. No. - 040 27990486

Fax - 040 27794388

Email - sales@synergytech.in

(b) Name and Full Address of Manufacturer :-

As per Schedule-B

(c) Brand: Agilent Technologies

4. Validity of Rate Contract: 01-JUN-11 To 31-MAY-12

5. Description of Item, Specification, Unit, Rate

Item Model No. No. Rs.)	Store Description	Description	Unit	Rate (in
1	Agilent 16801A with 2 qty of E5383A	Logic Analyzers	Description: Logic Analyzer 34 Channels NOS.	678568 Rs. SIX LAKHS SEVENTY- EIGHT THOUSAN D FIVE HUNDRED SIXTY- EIGHT ONLY
2	Agilent 16802A with 4 qty of E5383A	Logic Analyzers	Description: Logic Analyzer 68 Channels NOS.	894614 Rs. EIGHT LAKHS NINETY-

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					FOUR THOUSAN D SIX HUNDRED FOURTEEN ONLY
3	Agilent 16803A with 6 qty of E5383A	Logic Analyzers	Description: Logic Analyzer 102 Channels	NOS.	1112509 Rs. ELEVEN LAKHS TWELVE THOUSAN D FIVE HUNDRED NINE ONLY
4	Agilent 16804A with 8 qty of E5383A	Logic Analyzers	Description: Logic Analyzer 136 Channels	NOS.	1336546 Rs. THIRTEEN LAKHS THIRTY- SIX THOUSAN D FIVE HUNDRED FORTY-SIX ONLY
5	Agilent E5346A	Add-on Items for Logic Analyzers	Description: Set of High density Mictor Probes for 34 Channels	NOS.	61700 Rs. SIXTY- ONE THOUSAN D SEVEN HUNDRED ONLY
10	Agilent 16801A- 016	Add-on Items for Logic Analyzers	Description: Memory upgrade from 512K to 8 Mega Points	NOS.	153000 Rs. ONE LAKHS FIFTY- THREE THOUSAN D ONLY
11	Agilent 16802A-032 (for 34 Channel Logic Analyzer)	Add-on Items for Logic Analyzers	Description: Memory upgrade from 512K to 32 M	NOS.	387000 Rs. THREE LAKHS EIGHTY- SEVEN THOUSAN D ONLY

6-Terms of Delivery:

Free delivery at consignee's end including installation,
commissioning and training.

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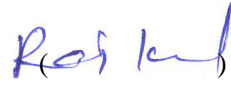
7-Excise Duty:	Not Applicable
8-Sales Tax:	CST/VAT Extra @4%
9-Delivery Period:	Within 12 to 16 weeks from the date of receipt of confirmed order, custom duty exemption certificate, road permit, End-user certificate & Octroi exemption certificate. In case of CDEC, the D/P will be counted from the date of issue of CDEC / EUC.
10(a)-Annual Turnover:	Without Limit
(b)-Monetary Limit(In Rs.):	Without Any Limit
11-Payment Terms:	As per Schedule-B
12-Slab Discount Clause:	Applicable
13-Prices:	FIXED
14-Quantity Offered:	NA
15-Minimum Quantity in Single Supply Order:	Without Limit
16-Minimum order Value in Single Supply Order:	Without Limit
17-Status of the RC Holding Firm:	LSI
18-Paying Authority:	The Deputy Controller of Accounts, Deptt. of Supply Shastri Bhawan, 26, Haddows Road Chennai - 600006
19-Inspection Authority:	For Civil ADG(QA),Jeevan Tara Building,Parliament Street New Delhi
20-Quality Assurance Officer:	For Civil Director(QA), Delhi Directorate, Jeevan Tara Building, 4th Floor, Sansad Marg New Delhi DELHI -110001
21-Place where the Stores are to be Tendered for Inspection:	Synergy Measurement Technologies Pvt. Ltd. 10/19, Automatic House, Ground Floor, East Patel Nagar, New Delhi- 110008.
22-R/C is DDOs Operated:	Yes

With effect from First October two thousand eight (01-10-2008),all supply order(s) against this Rate Contract must be placed by Direct Demanding Officer(s) on-line through D.G.S&D web site (www.dgsnd.gov.in) (indentor's page) only. Supply order(s) in any other form placed on or after 01-10-2008 shall not be valid and shall not be acted upon by the contractor or any other concerned agencies.

DDO shall download the supply order and send an ink signed copy to the concerned paying authority specified in the rate contract through Registered / speed post immediately after on-line placement of Supply Order(s).

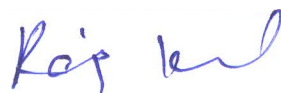
23-Packing & Specification :	see annexure
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Asstt. Director (S) / Section Officer / Dy. Director
For and behalf of the purchaser named in the Form DGS&D 1001.

All the R/C particulars including prices in respect of individual R/Cs are available on DGS&D website which can be accessed by all. The DGS&D website is <http://dgsnd.gov.in>



SCHEDULE - B

RC Specification: 1. Name & address of manufacturers:

i. M/s. Agilent Technologies (Chengdu) Co., Ltd. 79 Baicao Road, Qianfeng park, Chengdu Hi-Tech industrial Development Zone (West), Chengdu 611731, China

ii. Agilent Technologies Microwave Products (M) Sdn Bhd Bayan Lepas Free Industrial Zone, Bayan Lepas 11900, Penang, Malaysia

SPECIAL CONDITION OF CONTRACT:

1. Country of Origin- Malaysia

Sales Office: Agilent Technologies Sales (Pte) Ltd. No. 1, Yishun Avenue, 7 Singapore

2. Custom Duty Exemption Discount :

(a) 17.7% - for Defence Customers with Zero percent duty as per custom notification 39/96, against valid customs duty exemption certificate.

(b) 10% for Education Customers where applicable duty is 9.4% as per custom notification 51/96, against valid customs duty exemption certificate.

3. PAYMENT TERMS: 98% payment will be made against proof of Inspection & provisional receipt certificate issued by the consignee for the receipt of the material. The balance 2% of the contract price of the stores will be paid on installation, commissioning and final acceptance of stores by the consignee and also submission of bank guarantee for the balance 2% of contract value valid for 2 months beyond the guarantee/warranty period. Indemnifying the purchaser against all losses incurred during the guarantee/warranty period stipulated in the contract.

4. Demonstration of the machines will be carried out free of cost at consignee's place.

5. Machines shall conform to Guarantee/Warranty clause for a period of 12 months from the date of installation / commissioning.

6. Other terms and conditions are as per Form DGS&D-1001.

7. Octroi Duty and Local Taxes:

Normally materials to be supplied to Government Department against Govt. Contracts are exempted from levy of town duty, Octroi duty, terminal tax and other levies of local bodies. The local Town/Municipal Body regulations at times, however, provide for such exemption only on production of such exemption certificate from any authorized officer. Contractors should ensure that stores ordered against contracts placed by this office are exempted from levy of Town duty/Octroi duty, Terminal tax or other local taxes and duties. Wherever, required, they should obtain the exemption certificate from the purchase officer or indentor concerned, to avoid payment of such local taxes or duties. Octroi, entry tax etc on the buyers account in the absence of relevant exemption certificate. Road permit, Waybill to be provided by DDO along with the order.

All other terms and conditions as per standard conditions in Form DGS&D-1001.

SCHEDULE -D

SPECIAL INSTRUCTIONS FOR TO R/C HOLDERS

1. Rate Contract holders are advised that before entertaining the supply order(s), they should ensure the availability of following

certificates from DDOs.

- a) They are Central Government Department drawing funds from Consolidated Fund of India.
 - b) The expenditure involved for the purchase has received the sanction of the competent financial authority.
 - c) The funds are available under the proper head in the sanctioned budget allotment for the year.
 - d) They have been fully authorized by the Department to sign the supply order and incur the liability in respect of the stores being ordered.
2. The rate contract holders should assist the consignee(s) in the installation and operation of the machine, including recommendations for accessories and voltage stabilizer.

(Raj Kamal)

Assistant Director (S)

For and on behalf of the purchaser named in the form DGS&D-1001.

Annexure-A, The detailed Specification of Model quoted in the RC

Item 1,

Description: 34 Channels, 4GHz Timing, 250MHz state, 1M depth Logic Analyzer

With 2x17 channels single ended flying leads

Model no. 16801A, E5383A

Make: Agilent Technology

Item 2,

Description: 68 Channels, 4GHz Timing, 250MHz state, 1M depth Logic Analyzer

With 4x17 channels single ended flying leads

Model no. 16802A, E5383A

Make: Agilent Technology

Item 3,

Description: 102 Channels, 4GHz Timing, 250MHz state, 1M depth Logic Analyzer

With 6x17 channels single ended flying leads

Model no. 16803A, E5383A

Make: Agilent Technology

Item 4,

Description: 136 Channels, 4GHz Timing, 250MHz state, 1M depth Logic Analyzer

With 8x17 channels single ended flying leads

Model no. 16804A, E5383A

Make: Agilent Technology

Item 5

Description: Set of High density Mictor Probes for 34 Channels

Agilent Model no. N5346A

Make: Agilent Technology

SCHEDULE - C

List of Parallel Rate Contract For Logic Analyzer

Sr.No.	NAME & FULL ADDRESS OF FIRM	R/C NO & DATE
1	SRB INSTRUMENTS PVT. LTD. B-28/34 Subhash Chowk, Laxmi Nagar, Delhi-110092 Mr. Rakesh Pandita: 09810190551 Mr. Vinod Pandita: 09810445563 E-mail: info@srb-instruments.com . DELHI 110092	LOGIC_ANA/IT-4/RC- D2070000/0512/84/F0560/1542 04-MAY-11
2	AGMATEL INDIA PVT LTD E-366, 2nd Floor, Nirman Vihar, Vikas Marg, Delhi 110092 Mr. A.K.Aggarwal- 9810054742, E-mail: aka@agmatel.com Ph:011-22462256 / 22456529 / 43064306 Fax:011-22019833 . DELHI 110092	LOGIC_ANA/IT-4/RC- D2070000/0512/84/F0177/1543 04-MAY-11
3	SYNERGY MEASUREMENT TECHNOLOGIES PVT. LTD. 25/C, Nicholson Road, Tarbund, Secunderabad-500009 (AP) E-mail: debashis@synergytech.in Tele No. 040-44444040/50/35 Mr. Debashis: 09830067183 . ANDHRA PRADESH 500015	LOGIC_ANA/IT-4/RC- D2070000/0512/84/F0372/1541 04-MAY-11

NOTE:

(1) Before placing supply order, DDOs are advised to check with DGS&D or on the website whether any further RCs have been awarded or amendments to Rate Contract have been issued.

(2) All the R/C particulars including prices in respect of individual R/C's are available on DGS&D website, which can be accessed by all. The access code of DGS&D website is <http://dgsnd.gov.in>.

ANNEXURE

Common requirements for all main equipments:

1. The supply shall include one set of operation and maintenance manual for each equipment.
2. Type test reports for each model shall be from a govt. lab. Type testing shall consists of verification of all the features & functional requirements including environmental tests . The environmental tests sequence will be as under :
Dry Heat Test : For 16 hrs. at a temp. of 55 degree C in accordance with IS:9000/ part-3/section-5/1977 (reaffirmed in 2004).
Cold Test : For 4 hrs. at a temp. of (-)10 degree C in accordance with IS:9000/ part-2/section-4/1977 (reaffirmed in 2004).
Damp Heat Test : For 2 Cycles of 24 hours (12+12h) each at a temp. of 40 degree C & 95 % RH in accordance with IS:9000/part-5/section-2/1981 (reaffirmed in 2004).

The Logic Analyzer shall be checked for all the functional requirements before and after 1-2 hours recovery period of the above environmental tests sequence. There shall be no deterioration in the functional parameters.

OEM Type Test reports with environmental testing to an equivalent International Standard shall also be acceptable in this case.

These report shall be required to be submitted to the concerned DQA at the time of registration and inspection.

Environmental test mentioned above do not include the the requirement of operating condition requirement mentioned for each item in various schedules.

Logic Analyzers

(A) Logic Analyzer 34 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

1. Channels : 34
2. Max. State Clock Rate : 235 MHz
3. Timing (all channels, all the time) : To be available
4. Capacitive Loading : < 2 pf
5. Setup/Hold time : Window: 1.5 ns adjustable
6. Simultaneous State and Timing Analysis : To be available
7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp,
Upgradeable
9. Setup/Hold violation : To be available
10. Trigger States : 16
11. Trigger Types : Word, Range, Glitch with storage, Set-
Up/Hold Violation, range, Group, Counter etc.
12. External Peripheral interfaces : LAN, Serial and USB,
13. PC Characteristics : 2GHz or higher Intel Celeron Processor,
512 MB, 80 GB HDD, Windows XP OS, (Open
Windows) CD-RW (Internal/External).
14. Display : 10.4 inch/26.5Cms, Active matrix TFT,

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LCD

- 15. Probe : 2 sets of 17 Channel General Purpose Passive Probe
- 16. Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
- 17. Power Requirement : 230 +/-10% V AC, 50 Hz
- 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.

(B) Logic Analyzer 68 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

- 1. Channels : 68
 - 2. Max. State Clock Rate : 235 MHz
 - 3. Timing (all channels, all the time) : To be available
 - 4. Capacitive Loading : < 2 pf
 - 5. Setup/Hold time : Window: 1.5 ns adjustable
 - 6. Simultaneous State and Timing Analysis : To be available
 - 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
 - 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp,
Upgradable
 - 9. Setup/Hold violation : To be available
 - 10. Trigger States : 16
 - 11. Trigger Types : Word, Range, Glitch with storage, Set-
Up/Hold Violation, Timer, range,
Group,
Counter etc.
 - 12. External Peripheral interfaces : LAN, Serial and USB,
 - 13. PC Characteristics : 2 GHz or higher Intel Celeron Processor, 512 MB, 80 GB HDD, Windows XP OS, (Open Windows) CD-
- RW(Internal/External)
- 14. Display : 10.4 inch/26.5Cms, Active matrix TFT, LCD
 - 15. Probe : 4 sets of 17 Channel General Purpose Passive Probe
 - 16. Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
 - 17. Power Requirement : 230 +/-10% V AC, 50 Hz
 - 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.

(C) Logic Analyzer 102 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

- 1. Channels : 102
- 2. Max. State Clock Rate : 235 MHz
- 3. Timing (all channels, all the time) : To be available
- 4. Capacitive Loading : < 2 pf
- 5. Setup/Hold time : Window: 1.5 ns adjustable

- 6. Simultaneous State and Timing Analysis : To be available
- 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
- 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp,
Upgradeable
- 9. Setup/Hold violation : To be available
- 10. Trigger States : 16
- 11. Trigger Types : Word, Range, Glitch with storage, Set-
Group,
Up/Hold Violation, Timer, range,
Counter etc.
- 12. External Peripheral interfaces : LAN, Serial and USB,
- 13. PC Characteristics :2 GHz or higher Intel Celeron Processor,512 MB, 80 GB HDD, Windows XP OS,(Open Windows) CD-
RW(Internal/External).
- 14. Display : 10.4 inch/26.5Cms, Active matrix TFT, LCD
- 15. Probe : 6 sets of 17 Channel General Purpose Passive Probe
- 16, Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
- 17. Power Requirement : 230 +/-10% V AC, 50 Hz
- 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.

(D) Logic Analyzer 136 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

- 1. Channels : 136
- 2. Max. State Clock Rate : 235 MHz
- 3. Timing (all channels, all the time) : To be available
- 4. Capacitive Loading : < 2 pf
- 5. Setup/Hold time : Window: 1.5 ns adjustable
- 6. Simultaneous State and Timing Analysis : To be available
- 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
- 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp,
Upgradeable
- 9. Setup/Hold violation : To be available
- 10. Trigger States : 16
- 11. Trigger Types : Word, Range, Glitch with storage, Set-
Group,
Up/Hold Violation, Timer, range,
Counter etc.
- 12. External Peripheral interfaces : LAN, Serial and USB,
- 13. PC Characteristics : 2 GHz or higher Intel Celeron Processor,512 MB, 80 GB HDD, Windows XP OS,(Open Windows) CD-
RW(Internal/External).
- 14. Display : 10.4 inch/26.5Cms, Active matrix TFT,

Key

- LCD
- 15. Probe : 8 sets of 17 Channel General Purpose Passive Probe
 - 16, Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
 - 17. Power Requirement : 230 +/-10% V AC, 50 Hz
 - 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.

Add-on Items for Logic Analyzers

Specification:

Add on items for the above logic Analyzers as per specification mentioned in the schedule against each items.

Ref 1

LIST OF ALL AMENDMENTS

For RC No. LOGIC_ANA/IT-4/RC-D2070000/0512/84/F0372/1541

NOTE: No Amendments issued till date 13-MAY-11

Raj Kuf